

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Masato SUGA**

Serial Number: **Not Yet Assigned**

Filed: **March 16, 2004**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING  
DIAGONAL DIRECTION WIRING AND LAYOUT METHOD  
THEREFOR**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 16, 2004

Sir:

In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and

Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references, except for the U.S. patent reference, is enclosed  
herewith.

In the event there are any fees due in connection with the filing of this paper, please  
charge Deposit Account No. 01-2340.

Respectfully submitted,  
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**23850**

PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (2)

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Atty. Docket No. 040068	Serial No. New Application
	Applicant(s): Masato SUGA	
	Filing Date: March 16, 2004	Group Art Unit: Not Yet Assigned

### U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	6,546,540	M. Igarashi, et al.	04/08/03			
_____	AB						
_____	AC						
_____	AD						

### FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AE	2001-142931	05/25/01	Japan	Yes-Abstract/Discussed in the specification/Corresponds to USP 6546540
_____	AF	2000-82743	03/21/00	Japan	Yes-Abstract/Discussed in the specification
_____	AG				
_____	AH				
_____	AI				

### OTHER DOCUMENTS

_____	AJ	
_____	AK	
Examiner		Date Considered